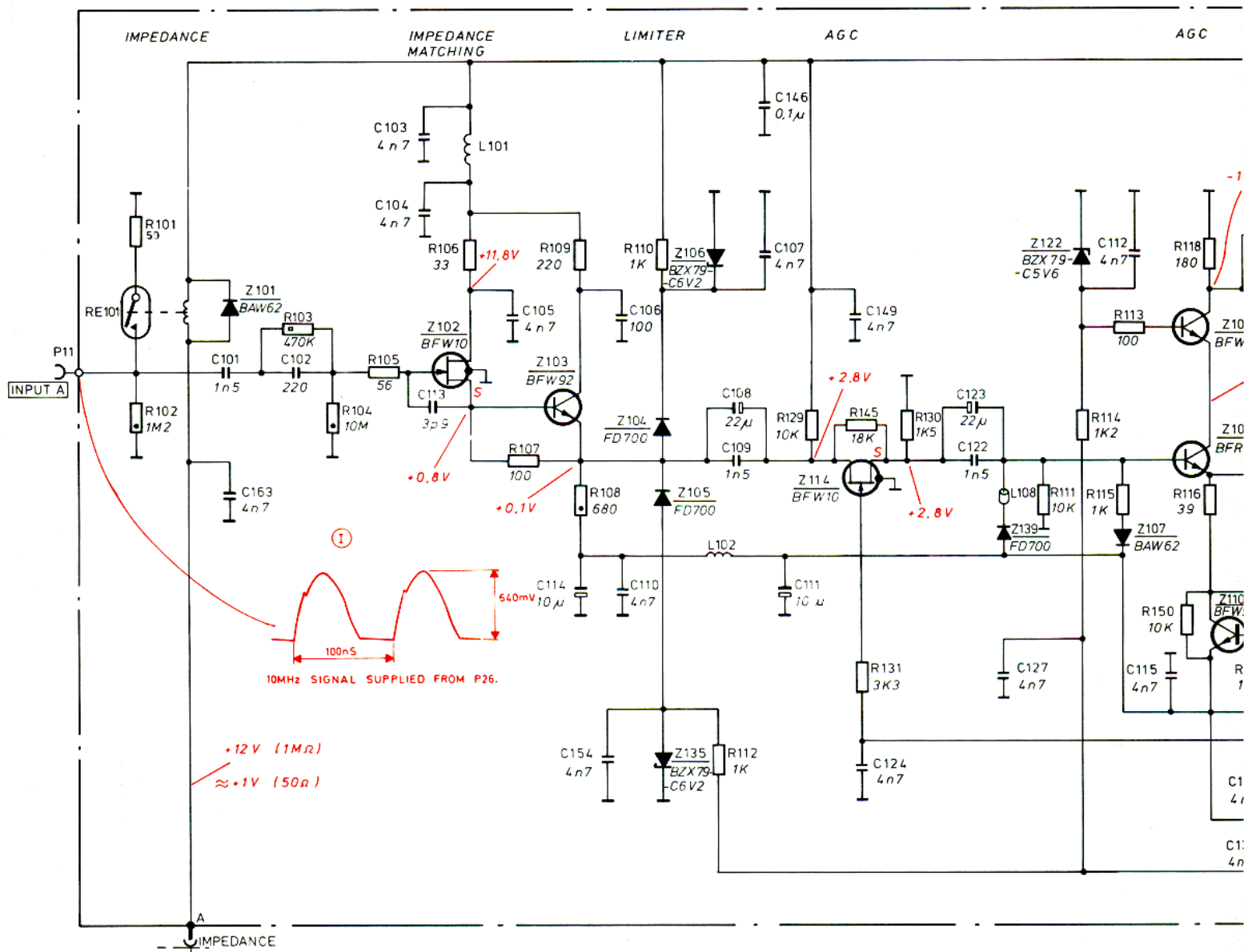




Fig. XVII-1. Printed wiring board, unit 1



TEST CONDITIONS

If not otherwise stated, the following test conditions apply:
 Measuring mode: FREQUENCY
 Gate time: 10 ms
 INT/EXT time base switch S413: INT
 10 MHz signal is taken from rear panel connector
 10 MHz OUT, P26, and applied to INPUT A, P11.
 D.C. voltages are measured without input signal.

TEST INSTRUMENTS USED

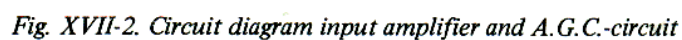
Waveform displays: PHILIPS sampling oscilloscope
 PM 3400 with passive probe 1:10,
 PM 9342.
 D.C. voltages : Multimeter 40,000 Ω/V, PHILIPS
 PM 2411

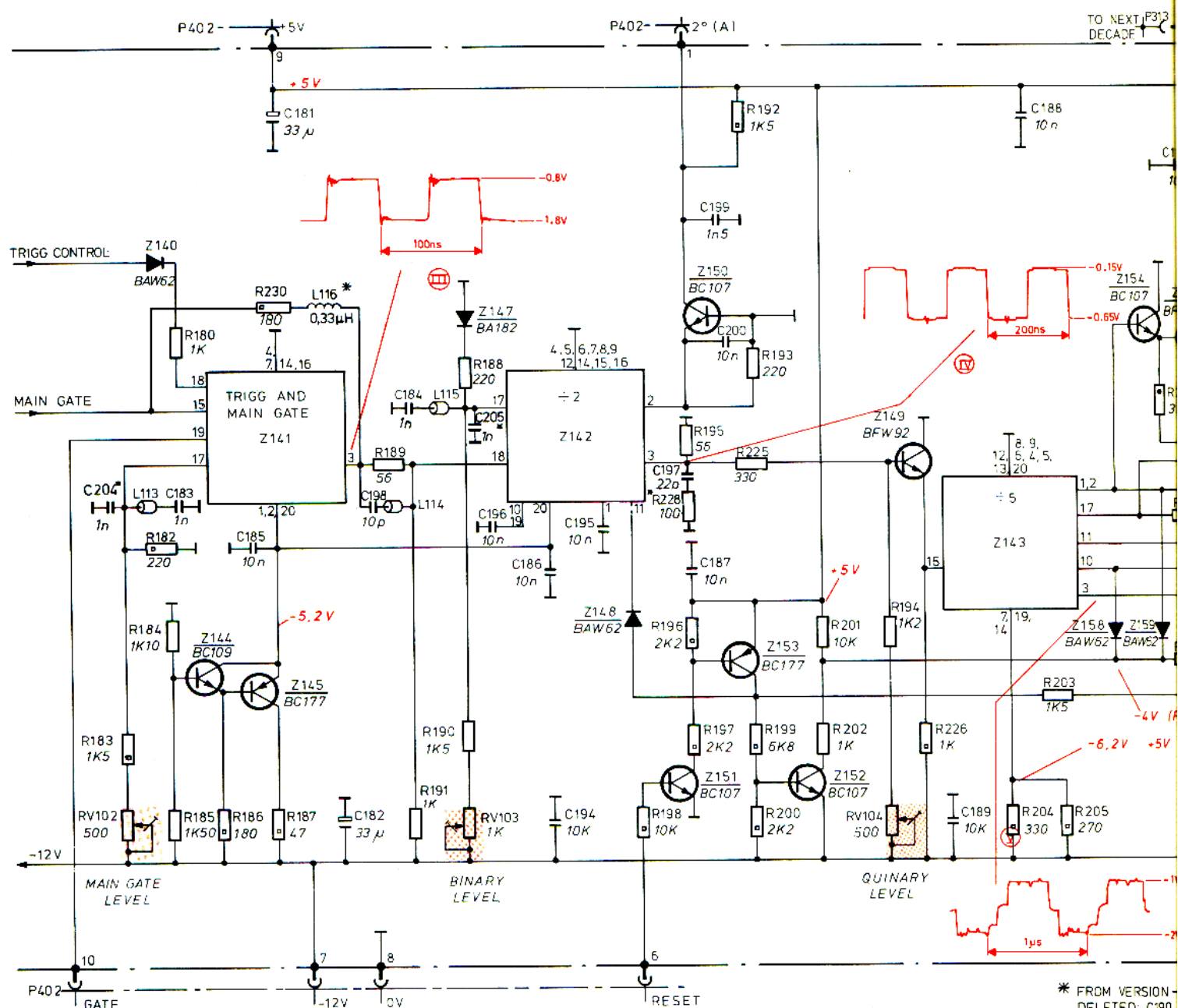
VOLTAGES AND WAVEFORMS ARE TYPICAL.

INPUT AMPLIFIERS



SUPPLY REGULATORS





* FROM VERSION -
DELETED: C190
CHANGED: R228
ADDED: L115

NOTE: R230
R225

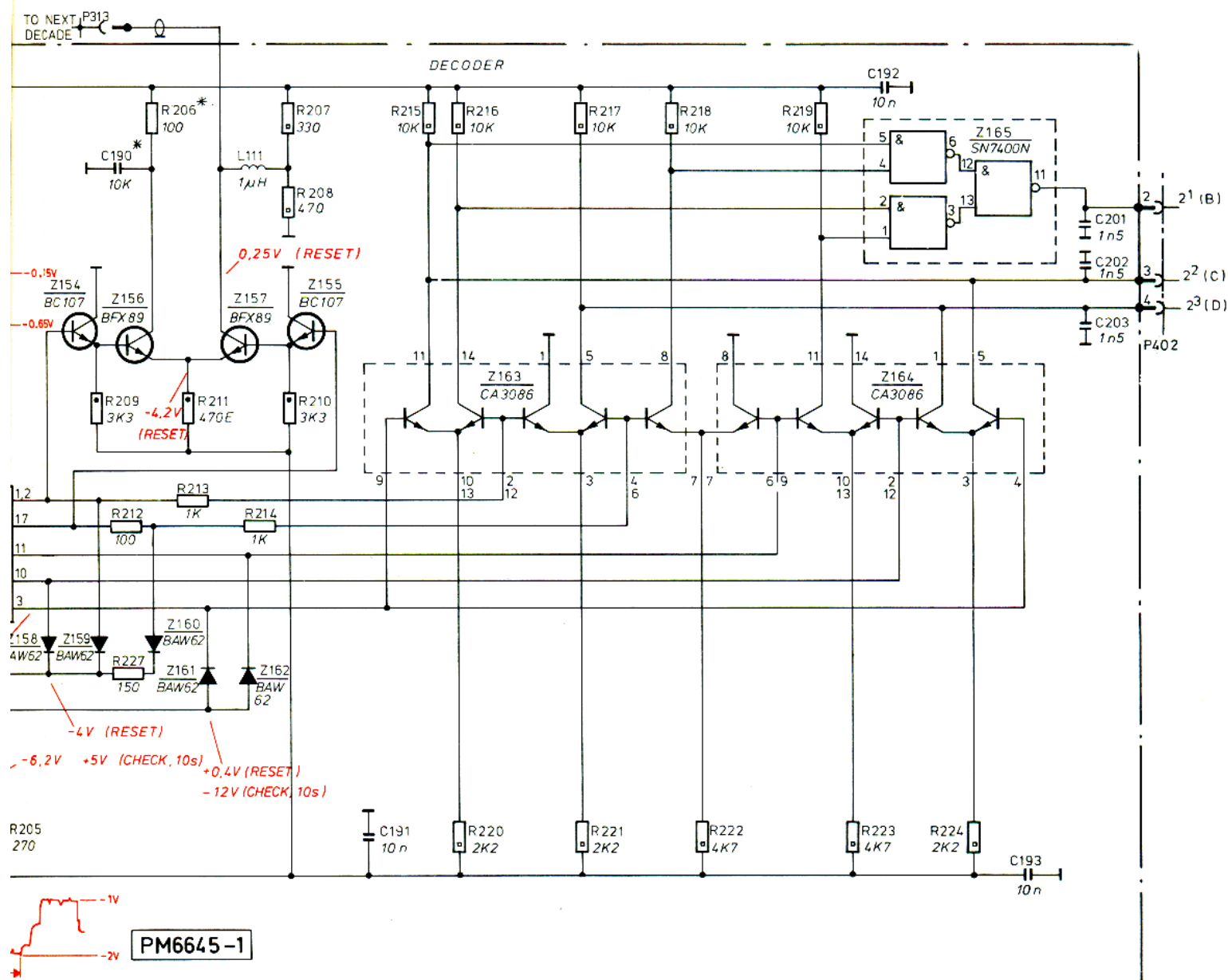
TEST CONDITIONS

If not otherwise stated, the following test conditions apply:
Measuring mode: FREQUENCY
Gate time: 10 ms
INT/EXT time base switch S413: INT
10 MHz signal is taken from rear panel connector
10 MHz OUT, P26, and applied to INPUT A, P11.
D.C. voltages are measured without input signal.

TEST INSTRUMENTS USED

Waveform displays: PHILIPS sampling oscilloscope
PM 3400 with passive probe 1:10,
PM 9342.
D.C. voltages : Multimeter 40,000 Ω/V, PHILIPS
PM 2411

VOLTAGES AND WAVEFORMS ARE TYPICAL.



FROM VERSION -/04
 DELETED: C190 R206
 CHANGED: R228 → 47Ω
 ADDED: L116 0,33μH

MA7392 A

MA7392

Fig. XVII-3. Circuit diagram main gate, trigger circuit, HF decade

NOTE: R230 present from version -/04.
 R225 is 82 ohm.

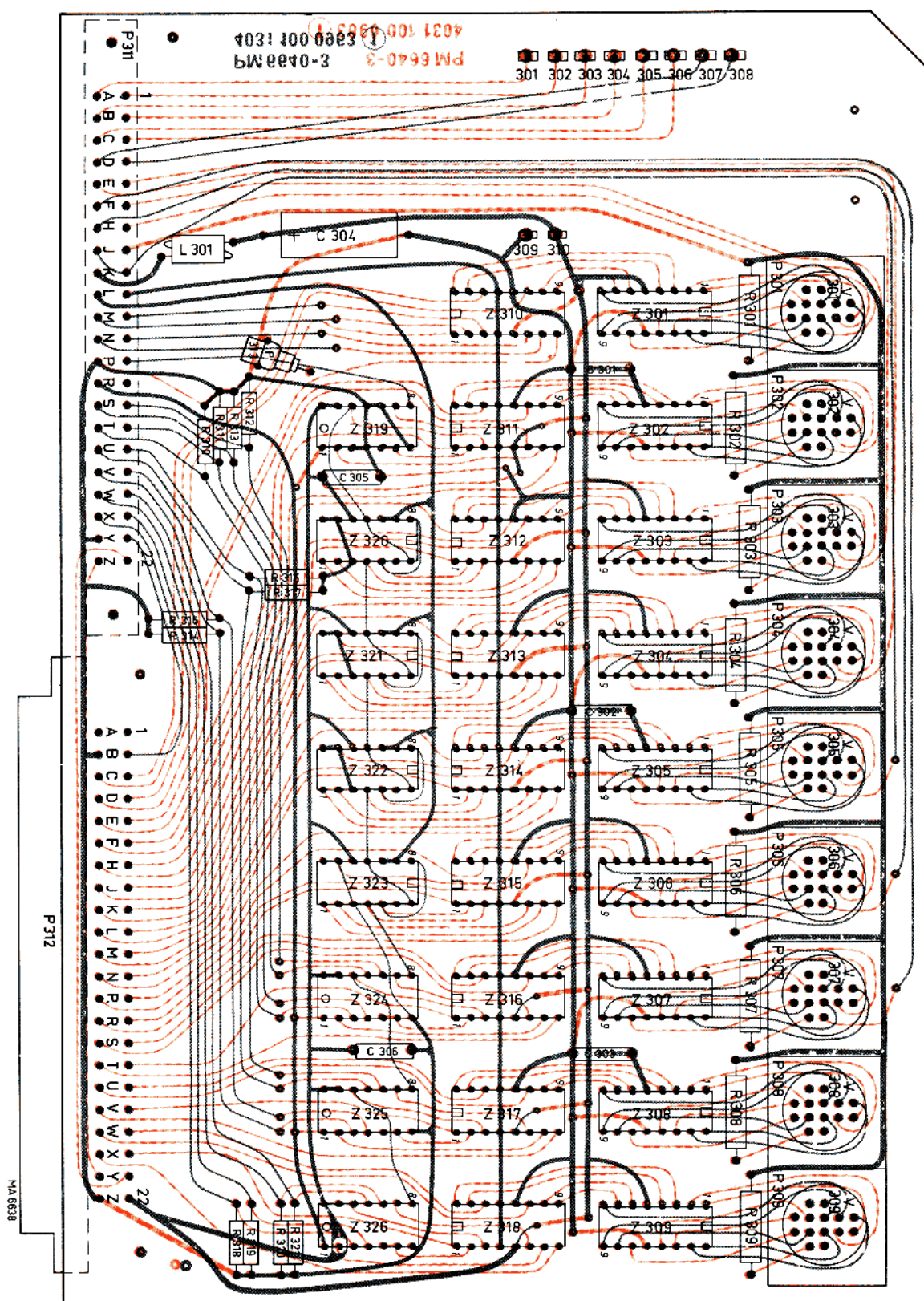
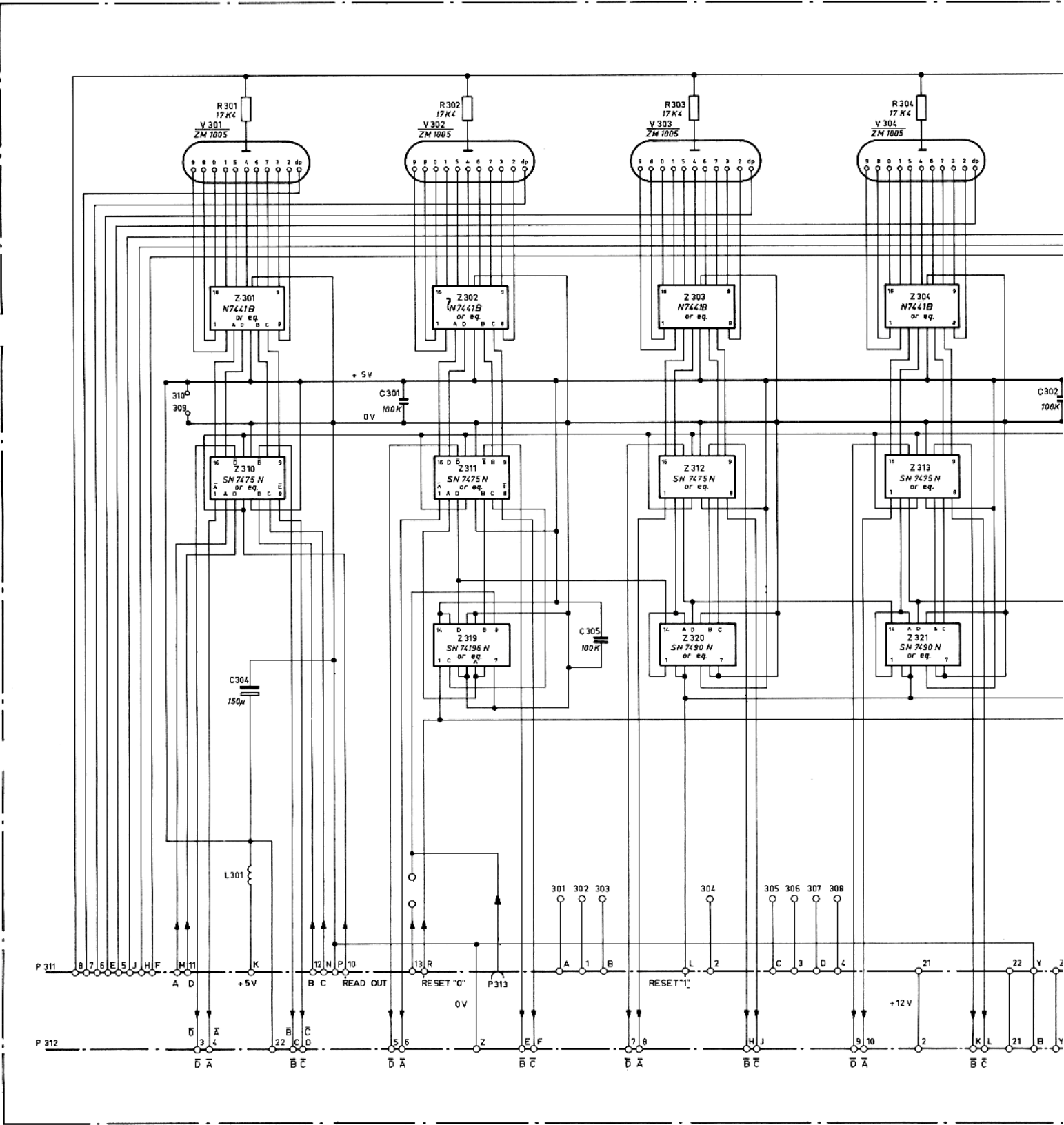
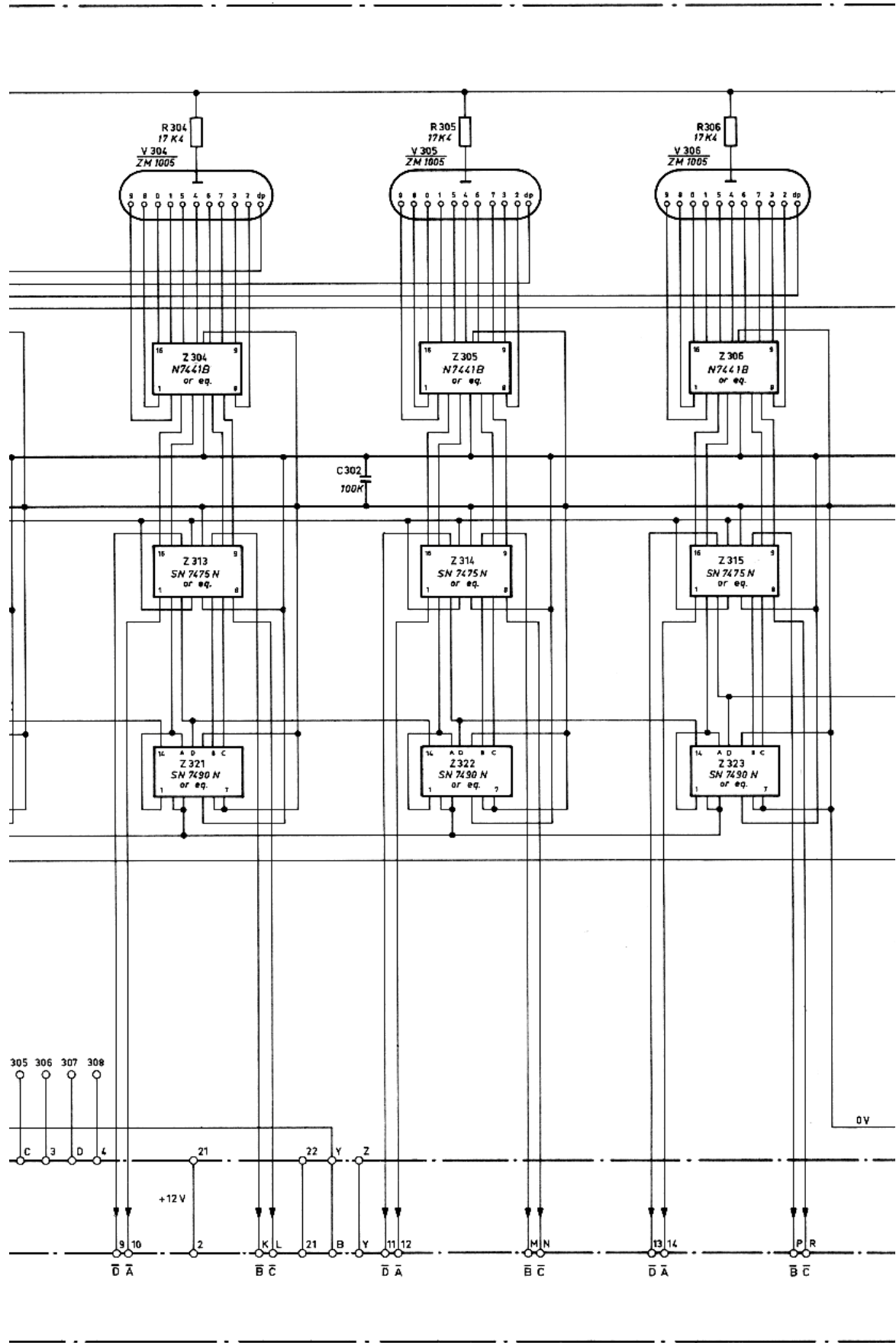


Fig. XVII-4. Printed wiring board, unit 3





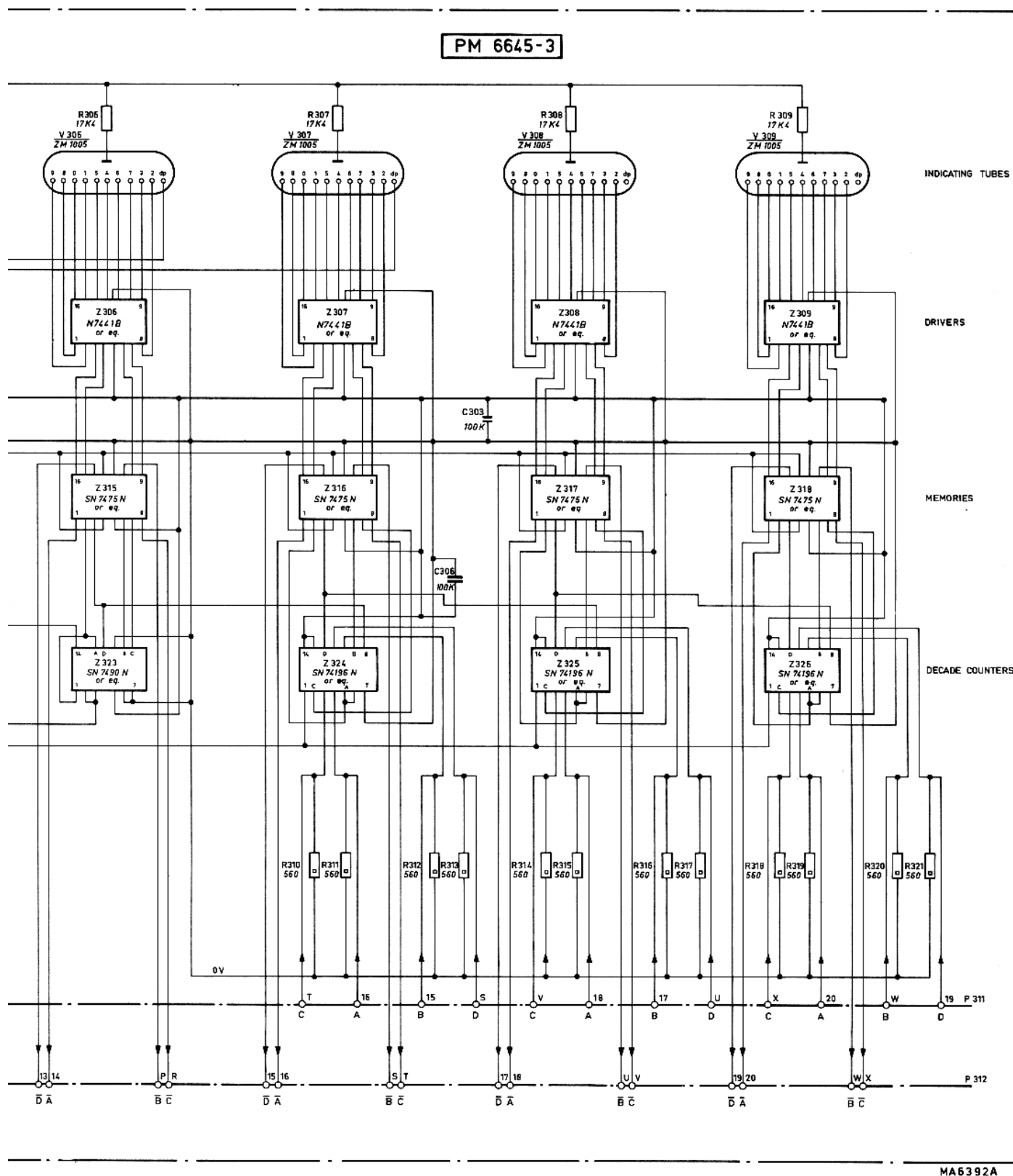
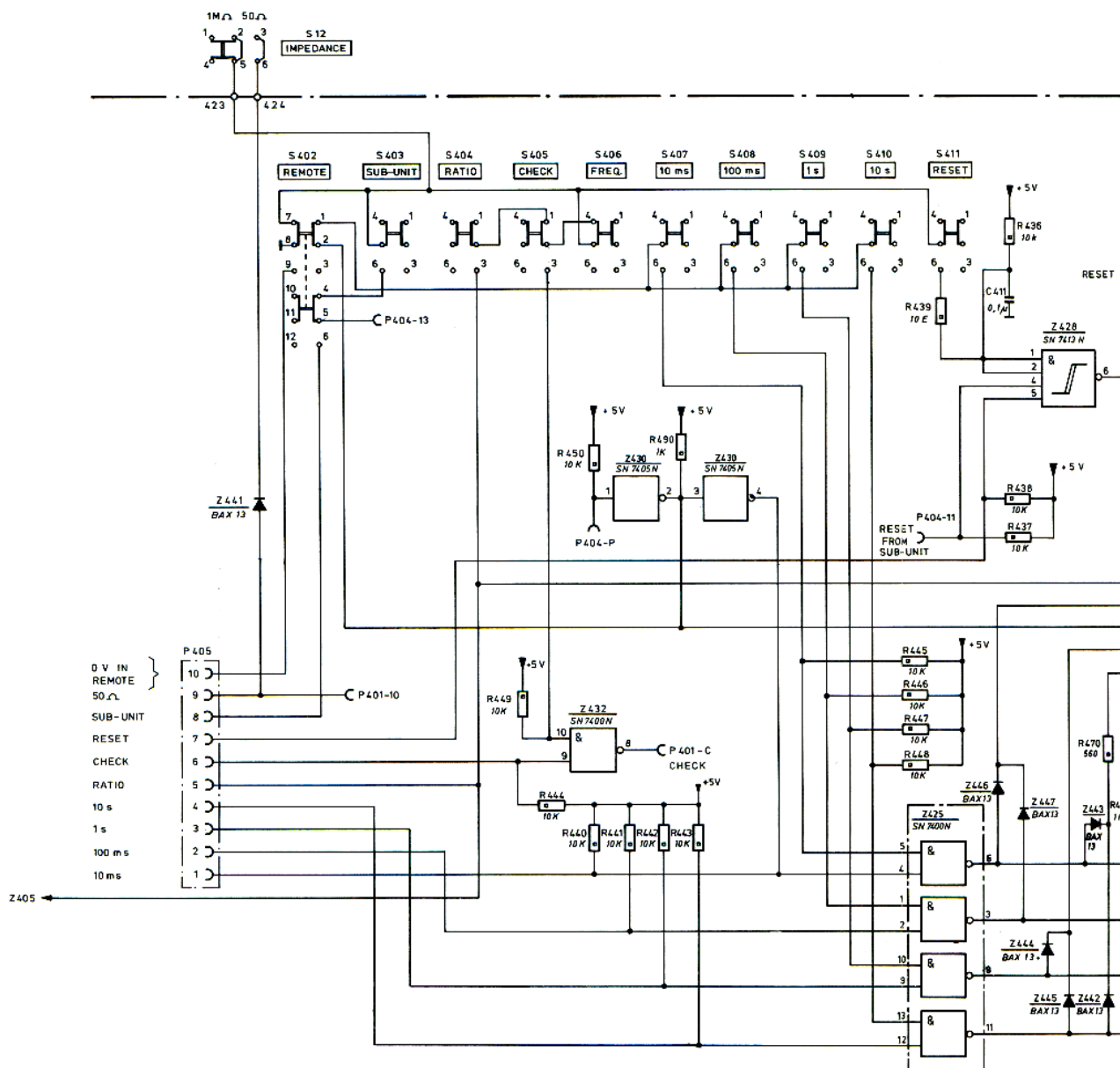


Fig. XVII-5. Circuit diagram counting and indicating assemblies

Fig. XVII-7. Printed wiring board, unit 4 (components side)



TEST CONDITIONS

If not otherwise stated, the following test conditions apply:

Measuring mode: FREQUENCY

Gate time: 10 ms

INT/EXT time base switch S413: INT

10 MHz signal is taken from rear panel connector

10 MHz OUT, P26, and applied to INPUT A, P11.

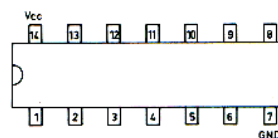
D.C. voltages are measured without input signal.

TEST INSTRUMENTS USED

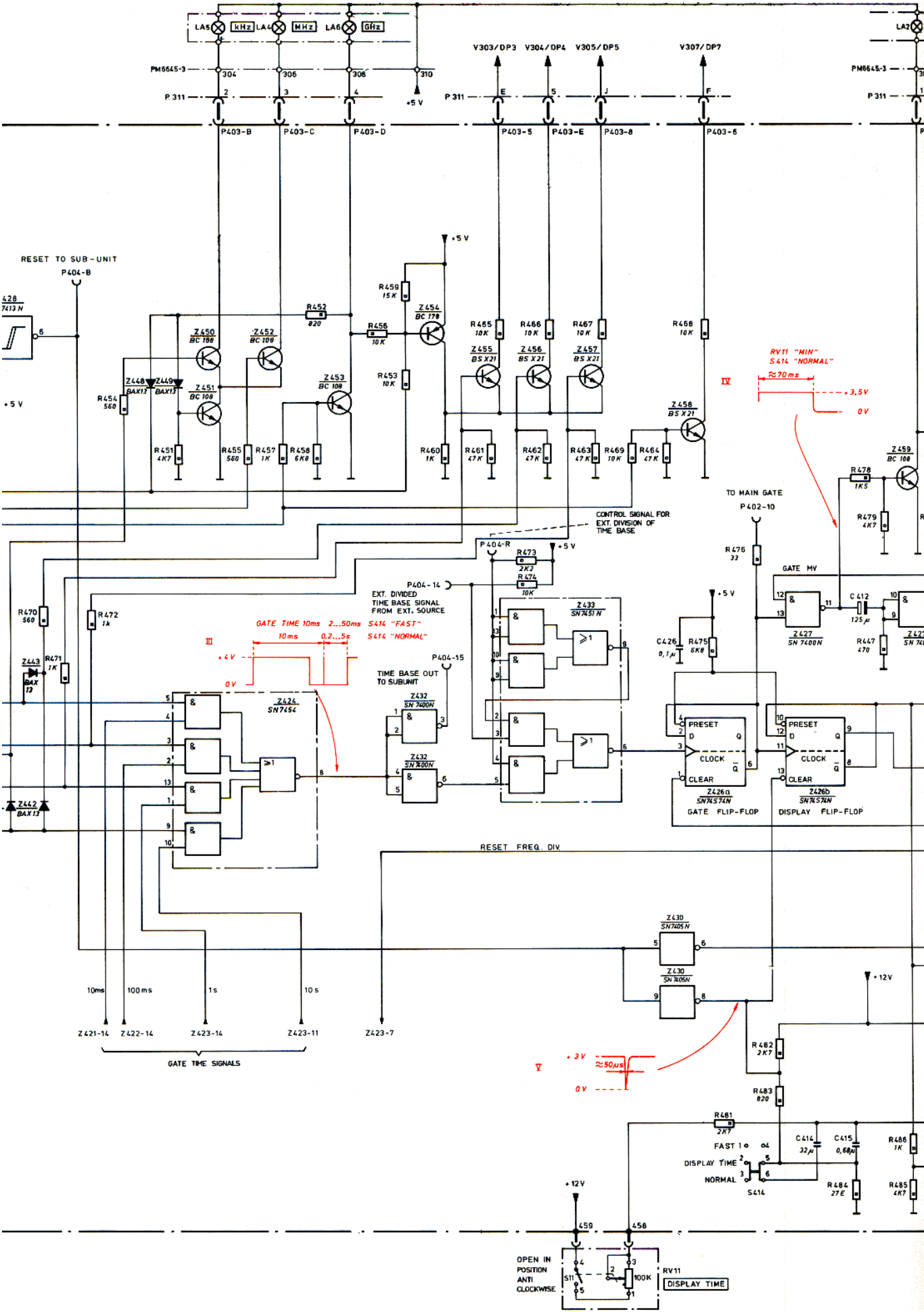
Waveform displays: PHILIPS sampling oscilloscope
PM 3400 with passive probe 1:10,
PM 9342.

D.C. voltages : Multimeter 40,000 Ω/V, PHILIPS
PM 2411

VOLTAGES AND WAVEFORMS ARE TYPICAL.



Z424, Z425, Z426, Z427, Z428, Z429
Z430, Z432, Z433



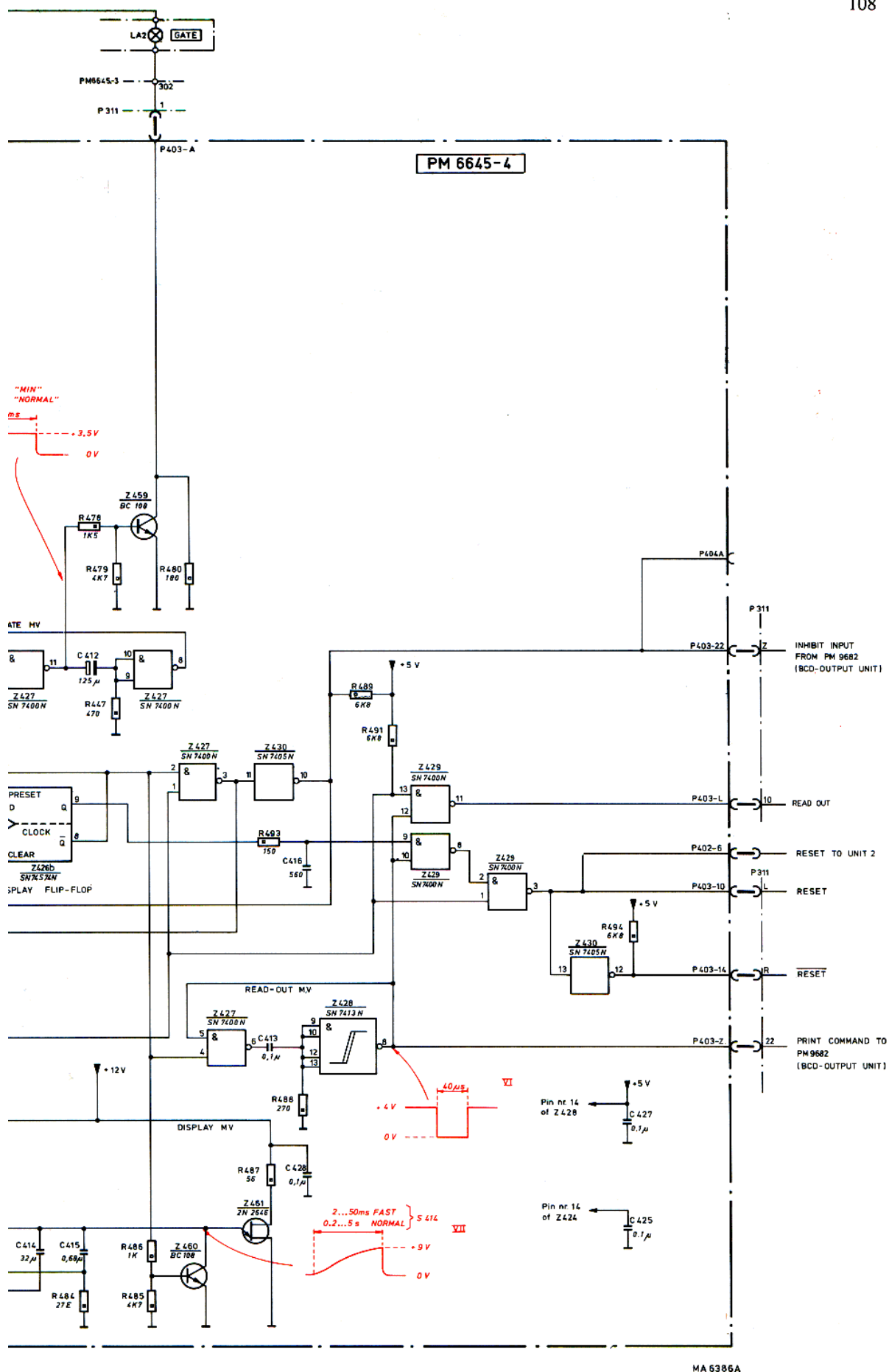
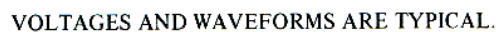


Fig. XVII-8. Circuit diagram control circuits



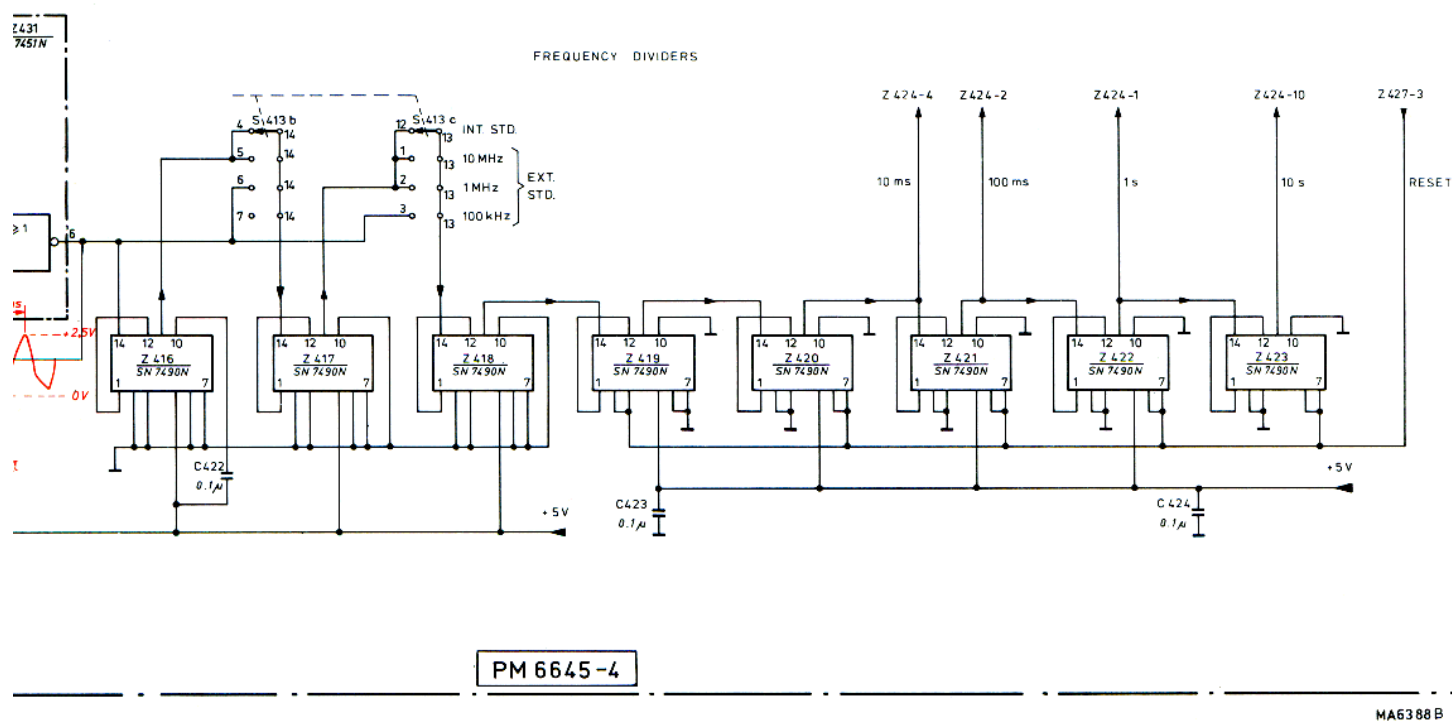


Fig. XVII-9. Circuit diagram crystal oscillator and time-base

i18

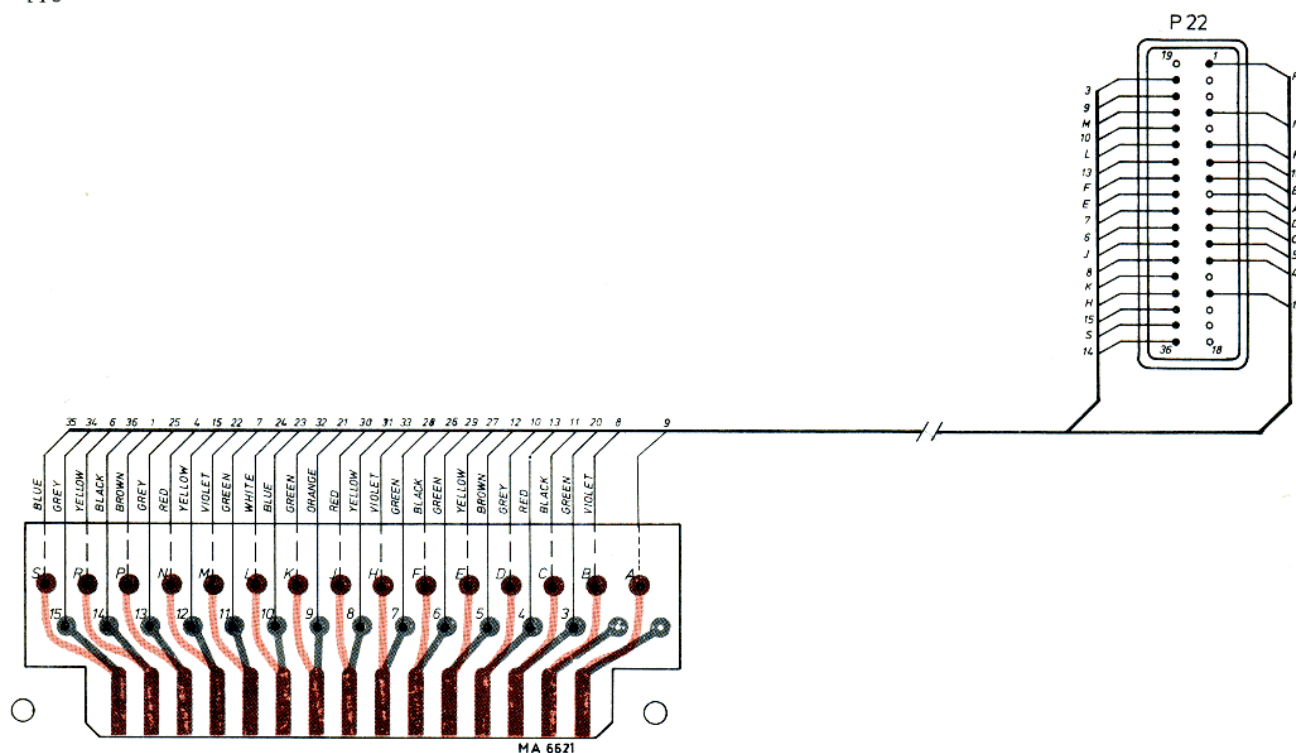


Fig. XVII-11. Printed wiring assy of sub-unit adaptation cable PM 9662

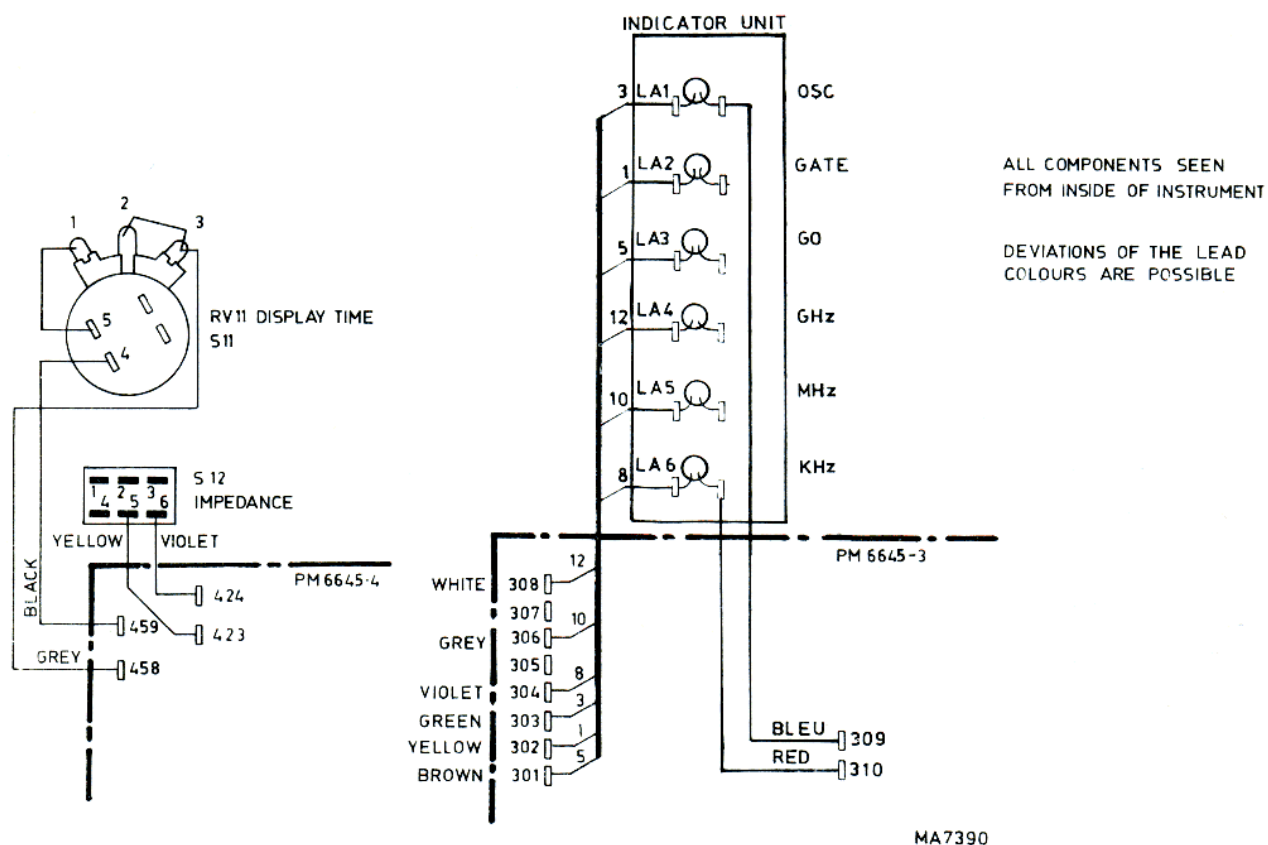
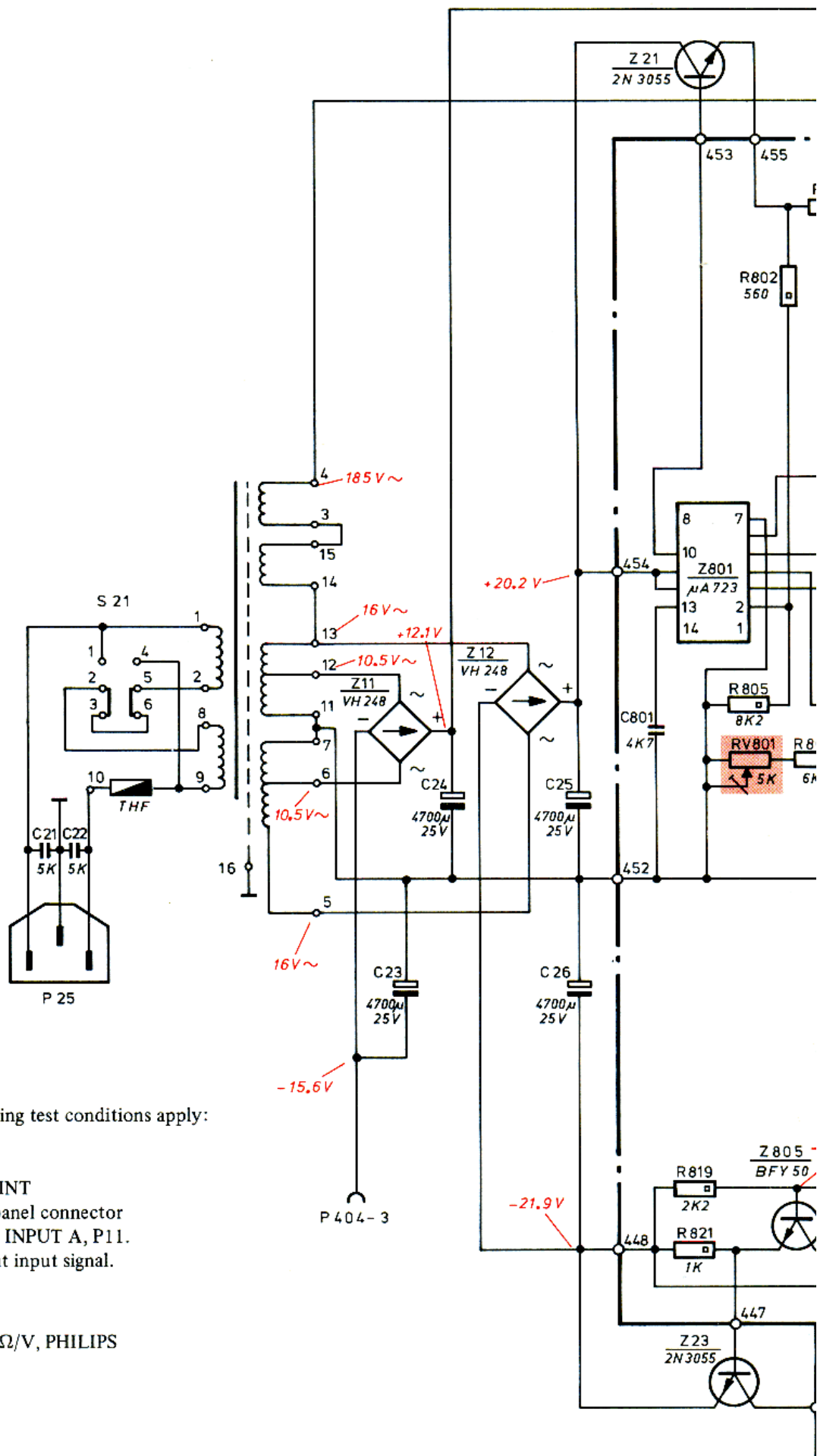


Fig. XVII-12. Wiring diagram indicator unit



TEST CONDITIONS

If not otherwise stated, the following test conditions apply:

Measuring mode: FREQUENCY

Gate time: 10 ms

INT/EXT time base switch S413: INT

10 MHz signal is taken from rear panel connector

10 MHz OUT, P26, and applied to INPUT A, P11.

D.C. voltages are measured without input signal.

TEST INSTRUMENTS USED

D.C. voltages: Multimeter 40,000 Ω/V, PHILIPS

PM 2411

VOLTAGES ARE TYPICAL.

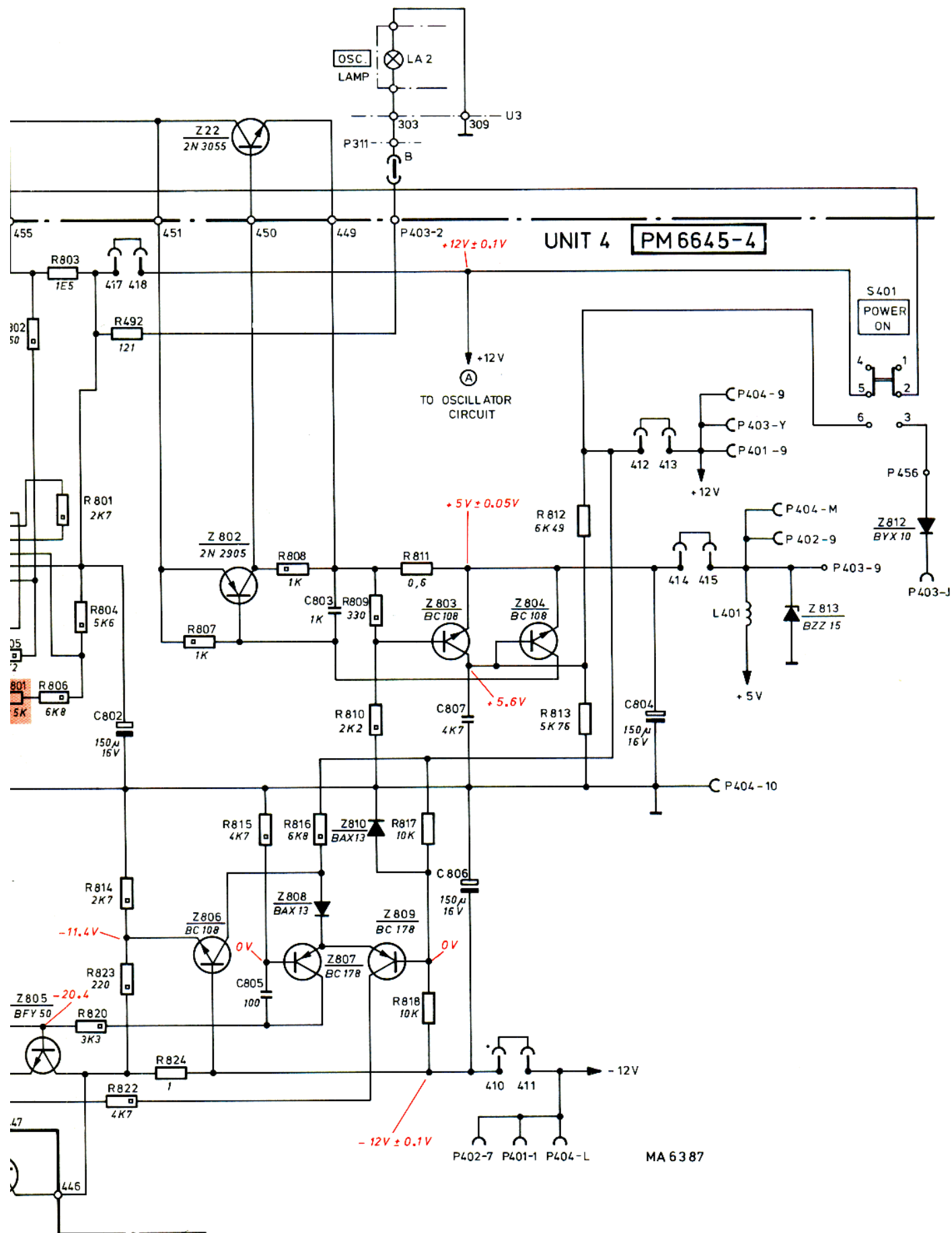


Fig. XVII-10. Circuit diagram power supply

